ı		
L		
ı		
ı		
1		
	_	
ı	>	-
ľ	0	
	يان	7
		7
L	6	)
ı	C	J
	_	
	U	1
	_	7
	=	2
	Ø	]
		7
ĺ	-	١,
ı		-
	-	=
ı	⋖	Ĺ
I	-	5

## PATENT NUMBER and ISSUE DATE

## U.S. UTILITY Patent Application

APPL NUM	FILING DATE		SUBCLASS	GAU	EXAMINER
10084787	02/25/2002	257	778	2811	William
**APPLICANT	S: Pendse	Rajendra	; Tam Samue	;	
İ					·
	G <mark>DATA VERIFIE</mark> I CLAIMS BENEFIT		2 236 02/27/2	201	
THIS APPEN C	CAINS BENEFIT	OF 60/2/	2,230 02/2//2	JU 1	
	•				
					* 23
** FOREIGN A	PPLICATIONS VE	RIFIED:			
***********	APT 40 400 1/00 1/00				
PG-PUS DO N	IOT FUBLISH		RESCI	ID 🗆	
Foreign priority clai 35 USC 119 condit	rned	•	s ii no s ii no		ATTORNEY DOCKET NO
	wledged Examiners's i	_	5 <b>-</b> 1 1:0		CPAC 1010-2 US
TiTLE : Supar-t	hin high speed flip	chip pac	kage		U.S.DEPT. OF COMM /PAY.& TM-PTO-436L(Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOW						
ISSUÉ FEE		Assistant Examiner	Total Claims		Print Claim for O.G			
			DRAWING					
Amount Due Date Paid	1	Sheets Drwg.	Figs.Drwg.	Print Fig.				
		Primary Examiner	Application Examiner					
TER	MINAL	PREPARED FOR ISSUE						
	DISCLAMER	WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.						

FILED WITH:

DISK (CRF)

F) CD-ROM (Attached in pocket on right inside flap)